

# System for the Verification of Control Algorithms via HIL- Simulation using PowerFactory

engineering innovative power solutions for a better world.

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## *Agenda*

- **Concept**
- Hardware-in-the-loop Concept
- Developed Hardware-in-the-loop System
- Real Time Simulation Results
- Prospect
- Summary

“The proof of concept of a hardware-in-the-loop simulation that allows testing of controller functionality within a simulated power system.”

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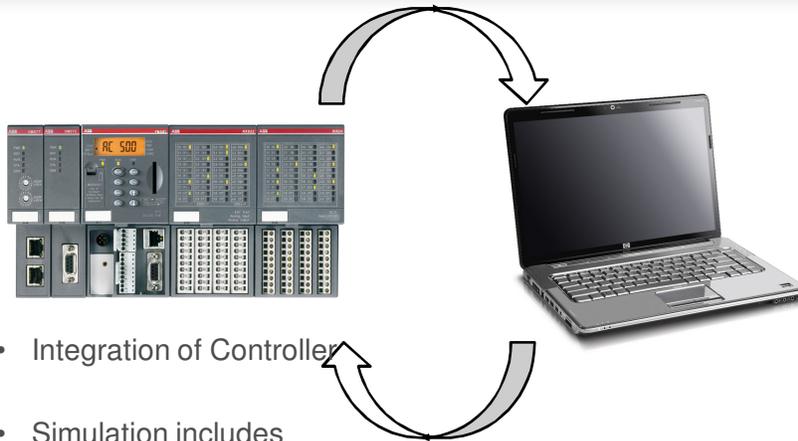
## Simulated Power System in PowerFactory

- PowerFactory running on Computer
- Reproducible Specific Event Scenarios
- Repeatable Tests
- Graphs
- Documentation



## Hardware-in-the-loop Simulation

- Integration of Controller
- Simulation includes Calculations from Controller



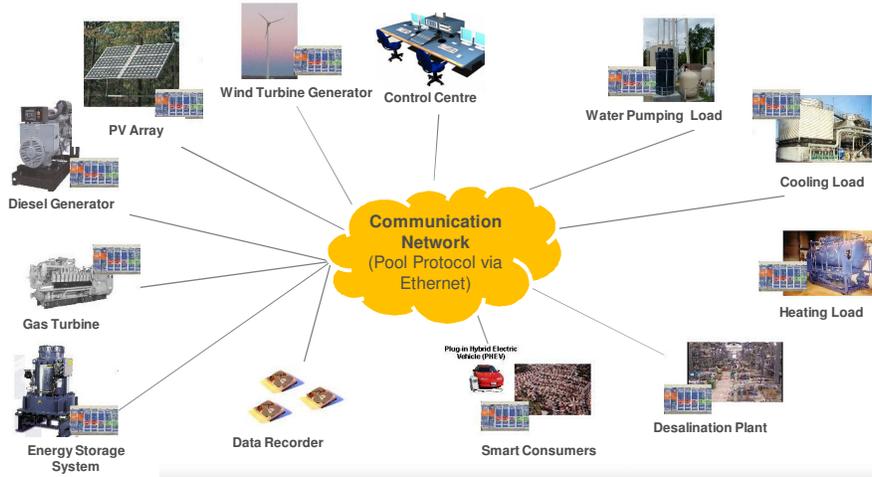
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## Power System Architecture

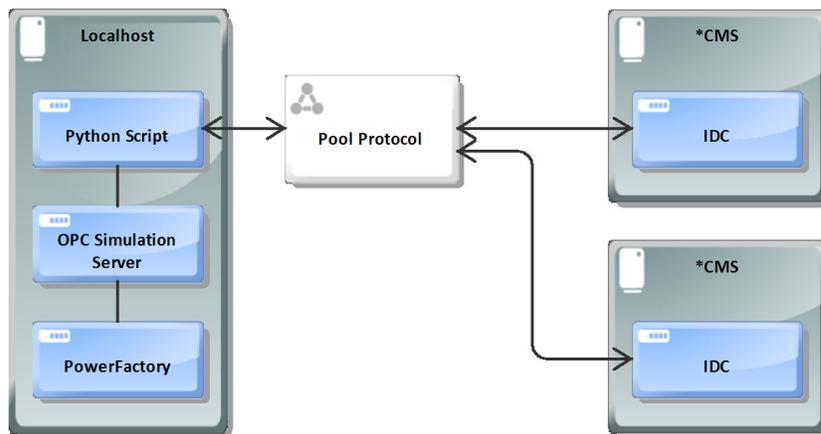


# Power System Architecture



# H-I-L System Architecture

Powercorp Distributed Control System (DCS)

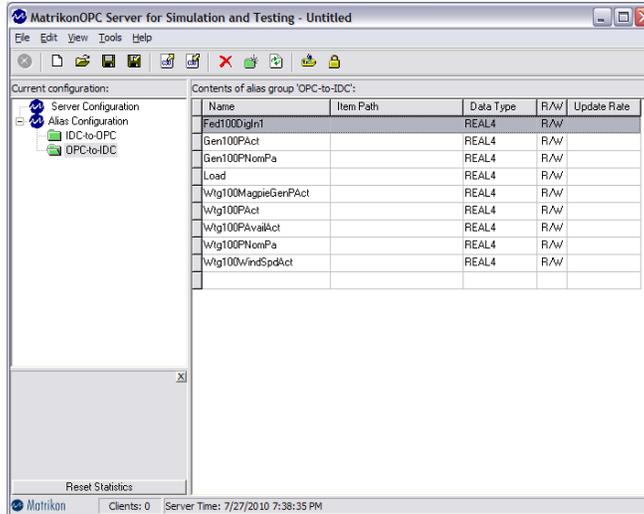


- Free Software & Free Support
- Runs on Windows Machines
- Accessible through the Network
- Names of Variables can be chosen as required.
- They have to match signals within PowerFactory

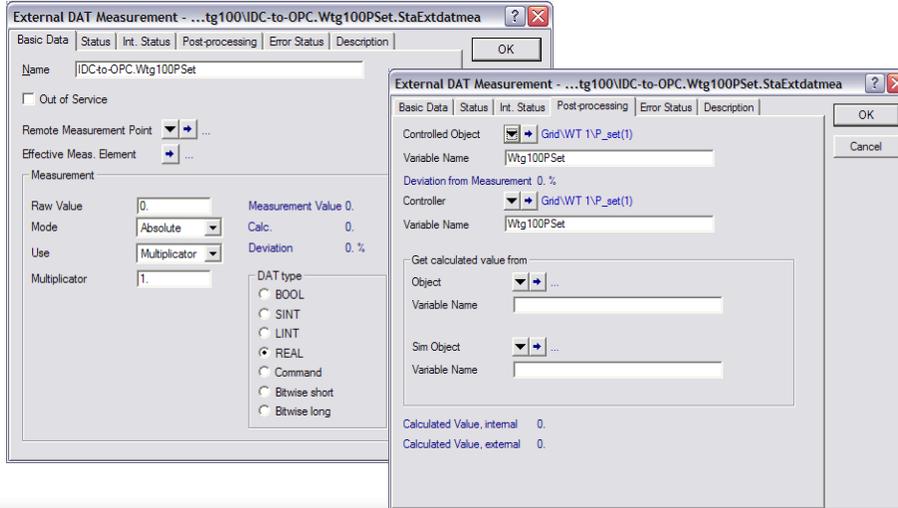


### Typical Data Definition

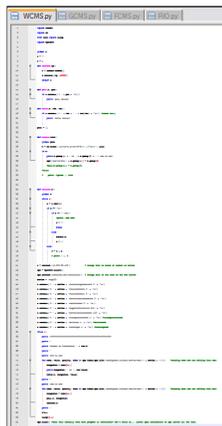
#	GroupName	AliasName	Item Path	Data Type	ReadOnly	PollAlways	UpdateRate
11	OPC-to-IDC	Wtg100PAct		4	0	0	0.0
12	OPC-to-IDC	Wtg100PavailAct		4	0	0	0.0
13	OPC-to-IDC	Wtg100WindSpdAct		4	0	0	0.0
14	OPC-to-IDC	Wtg100NomPa		4	0	0	0.0
15	OPC-to-IDC	Wtg100MaggieGenPAct		4	0	0	0.0
16	OPC-to-IDC	Gen100NomPa		4	0	0	0.0
17	OPC-to-IDC	Gen100PAct		4	0	0	0.0
18	OPC-to-IDC	Fed100Digin1		4	0	0	0.0
19	OPC-to-IDC	Load		4	0	0	0.0
20	IDC-to-OPC	Wtg100PSet		4	0	0	0.0
21	IDC-to-OPC	Fed100DigOut0		4	0	0	0.0
22	IDC-to-OPC	Fed100DigOut1		4	0	0	0.0
23	IDC-to-OPC	Rio100Digin4		4	0	0	0.0
24	IDC-to-OPC	Wtg100MaggieGenPAct		4	0	0	0.0
25	IDC-to-OPC	Wtg100MaggieGenPAct		4	0	0	0.0



## Interaction to OPC Server



## Python Script



Looking for variables

## OPC Server

#	GroupName	AliasName	Item Path	Data Type	ReadOnly	PollAlways	UpdateRate
11	OPC-to-IDC	Wtg100PAct		4	0	0	0.0
12	OPC-to-IDC	Wtg100PAvailAct		4	0	0	0.0
13	OPC-to-IDC	Wtg100WindSpdAct		4	0	0	0.0
14	OPC-to-IDC	Wtg100PNomPa		4	0	0	0.0
15	OPC-to-IDC	Wtg100PNomPa		4	0	0	0.0
16	OPC-to-IDC	Wtg100MagPieGenPAct		4	0	0	0.0
17	OPC-to-IDC	Gen100PNomPa		4	0	0	0.0
18	OPC-to-IDC	Gen100PAct		4	0	0	0.0
19	OPC-to-IDC	Fed100Dign1		4	0	0	0.0
20	OPC-to-IDC	Load		4	0	0	0.0
21	IDC-to-OPC	Wtg100PSet		4	0	0	0.0
22	IDC-to-OPC	Fed100DignOut0		4	0	0	0.0
23	IDC-to-OPC	Fed100DignOut1		4	0	0	0.0
24	IDC-to-OPC	Rio100Dign4		4	0	0	0.0
25	IDC-to-OPC	Wtg100MagPieGenPAct		4	0	0	0.0

Python Script

# Python Script

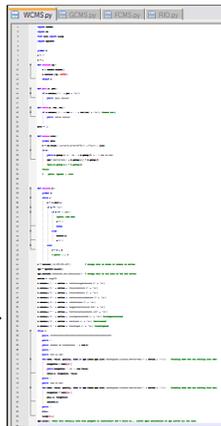
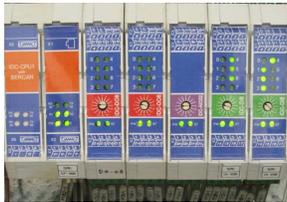
```

While(1)
{
  for(items in OPC-to-IDC)
    read-var-val(OPC-to-IDC) // From the OPC Server
    write(variables, values) // = Wtg1PAct 300

  for(items in IDC-to-OPC)
    read-var(IDC-to-OPC) // From the OPC Server
    poll-for-val(variable) // ? Wtg1PSet
    write(variable, value) // Write into OPC Server
}

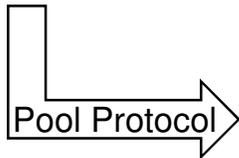
```

# Group IDC-to-OPC

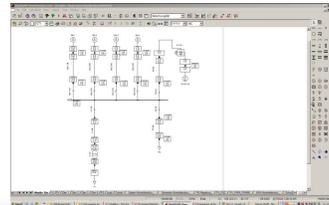


Wtg1PSet = 200

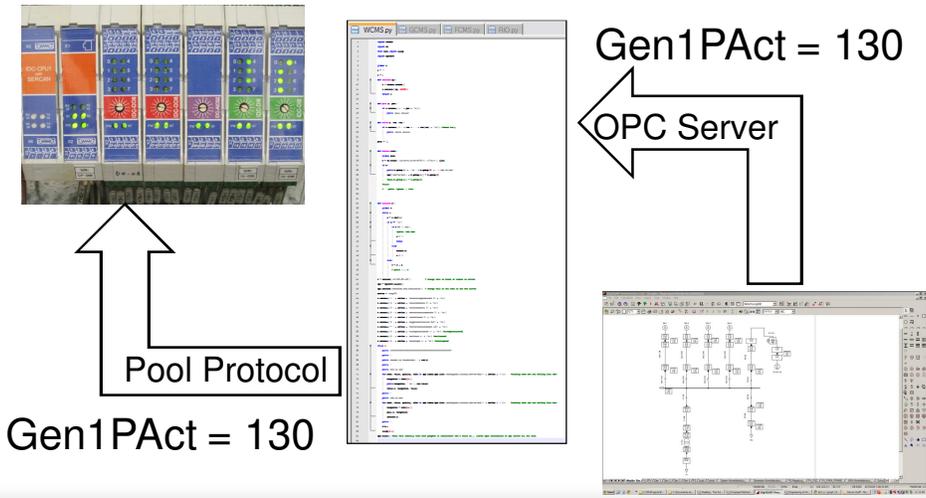
OPC Server



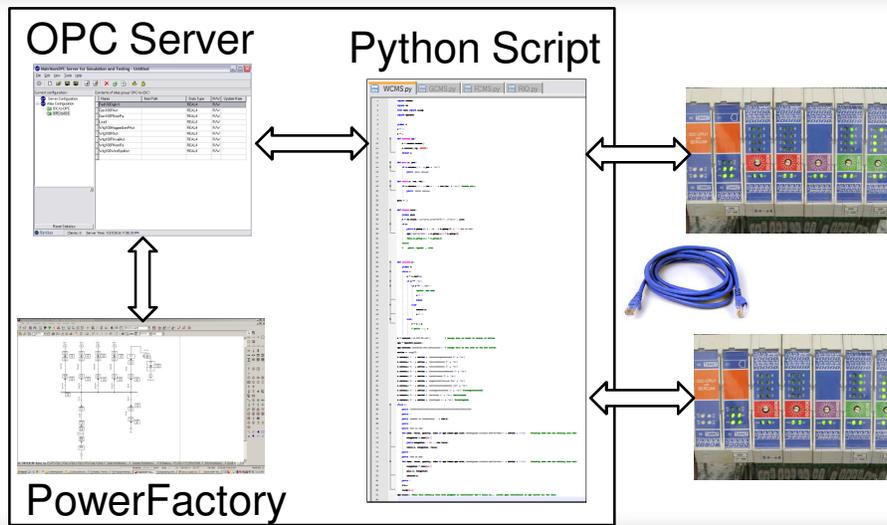
Wtg1PSet = 200



## Group OPC-to-IDC



## H-I-L System Architecture



## The actual test board

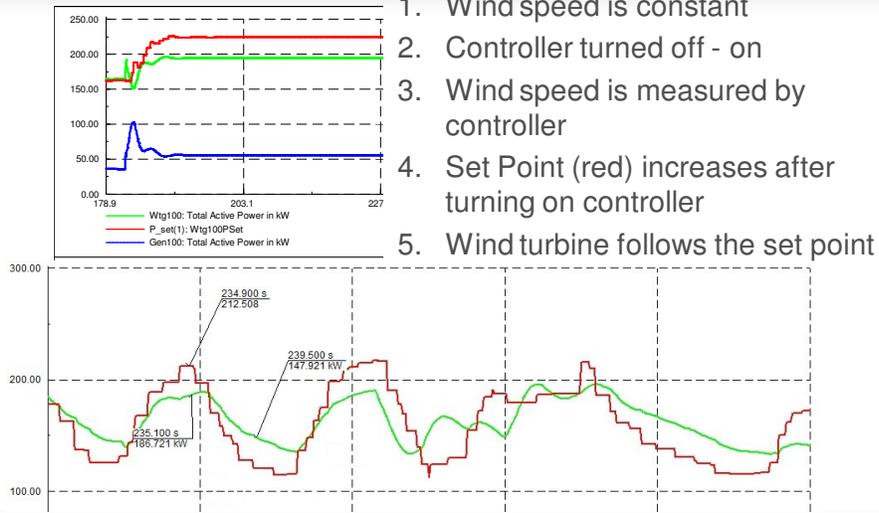


- Network Switch
- GCMS
- WCMS
- FCMS
- RIO (CB status, CB close for load step)
- Circuit Breaker
- Power Supply

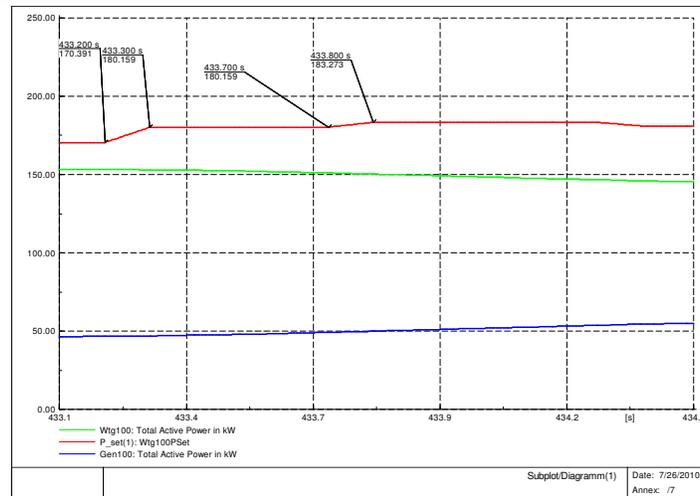
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## Successful Interaction



## Communication Speed



- Thesis Statement
- Hardware-in-the-loop Concept
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- Use as Development Tool for new Control Algorithms
  - New Control Algorithms can be tested against a Real Power System
- Show System for Customers
  - Education of Customers
  - Demonstration of the DCS Functionality and its Value
- Hardware Testing Platform
  - Use as tool to test new Hardware before Commitment to a complete system

- How we do controller development now:
  - *Writing* of the distributed control Requirements Specification
  - *Adding* relevant Use Case Scenarios
  - *Propose* control algorithms
  - *Analyse* the algorithms (first analysis of performance)
  - *Implement* the control algorithms within PowerFactory
  - *Measure* their performance by simulating operation within PowerFactory (second analysis of performance)
  - *Implement* the final algorithms on the controller hardware
  - *Run* the power system model with H-I-L operation of the controller hardware in PowerFactory in real-time
  - *Test* against the Use Case Scenarios (third analysis of performance)

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- Proof of Concept – It's Working!

but

- Improvements can be made – It's a Proof of Concept!



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